

SREENIVASA INSTITUTE OF TECHNOLOGY AND MANAGEMENT STUDIES, CHITTOOR



(AUTONOMOUS)

Approved by AICTE, New Delhi, Affiliated to JNTUA, Ananthapuramu.

ESTD: 1998

EXAMINATION SECTION

Date: 01.07.2024

M.TECH (R20) I Year -I - Semester - Semester End Supplementary Exam - July 2024

Exam Schedule	M.TECH (CSE)	M.TECH (PE)	M.TECH (VLSI)	M.TECH (PD)
11.07.2024 (Thursday) 02.00 PM to 5.00 PM	Mathematical Foundations of Computer Science (20MCSE11)	Analysis of Power Electronic Converter (20 MPE011)	CMOS VLSI and Digital IC Design (20MVSD11)	Creativity in Product Design (20 MPDD11)
13.07.2024 (Saturday) 02.00 PM to 5.00 PM	Advanced Data Structures and Algorithms (20MCSE12)	Power Electronic Control of DC Drives (20MPE012)	Analog IC Design (20 MVSD12)	Computer Applications in Product Design (20MPDD12)
16.07.2024 (Tuesday) 02.00 PM to 5.00 PM	Java and Web Technologies (20MCSE13)	Control System Design for Power Electronics (20MEPE13)	FPGA and HDL Programming (20MVSD13)	Material Selection for Product Development (20MPDD13)
19.07.2024 (Friday) 02.00 PM to 5.00 PM	Research Methodology and IPR (20MMAC11)	Research Methodology and IPR (20MMAC11)	Research Methodology and IPR (20MMAC11)	Research Methodology and IPR (20MMAC11)
23.07.2024 (Tuesday) 02.00 PM to 5.00 PM	Artificial Intelligence (20MCSE14B)/ Wireless Sensor Network (20MCSE14A)	Power Quality Improvement (20MPE015D)	Advanced Digital System Design (20MVSD14D)	Design for Manufacture Assembly and Environments (20 MPDD14 A)
25.07.2024 (Thursday) 02.00 PM to 5.00 PM	Advanced Compiler Design (20MCSE15 B)	Solar Energy Conversion System (20MPE016B)	Design for Testability and Testing (20MVSD15A)	Design of Hybrid ad Electric Vehicles (20MPDD15C)

Controller of Examinations

Principal

Copy to:

- 1. Copy to Dean (Academics) and all HODs of ME, EEE, ECE and CSE with a request to arrange to read in the respective classroom.
- 2. Circulation among the faculty members and display on the notice board. Copy to Computer Centre to upload the same in the college website.