

SREENIVASA INSTITUTE OF TECHNOLOGY & MANAGEMENT STUDIES (AUTONOMOUS) CHITTOOR I YEAR M.TECH I SEMESTER SUPPLEMENTARY (R20) -SEPTEMBER -2023 EXAMINATIONS Dt: 21-08-2023 Time-table

R20

DATE / TIME	Power Electronics (EEE)	Product Design (ME)	VLSI System Design (ECE)	CSE
08-09-2023 (2.00 PM to 5.00 PM (Friday)	Analysis of Power Electronic Converter (20 MPE011)	Creativity in Product Design (20 MPDD11)	CMOS VLSI and Digital IC Design (20MVSD11)	Mathematical Foundations of Computer Science (20MCSE11)
11-09-2023 (2.00 PM to 5.00 PM (Monday)	Power Electronic Control of DC Drives (20 MPE012)	Computer Applications in Product Design (20 MPDD 12)	Analog IC Design (20 MVSD12)	Advanced Data Structures and Algorithms (20 MCSE 12)
13-09-2023 (2.00 PM to 5.00 PM (Wednesday)	Control System Design for Power Electronics (20 MEPE 13)	Material Selection for Product Development (20 MPDD 13)	FPGA and HDL Programming (20 MVSD13)	Java and Web Technologies (20 MCSE 13)
15-09-2023 (2.00 PM to 5.00 PM (Friday)	Research Methodology and IPR (20 MMAC 11)	Research Methodology and IPR (20 MMAC 11)	Research Methodology and IPR (20 MMAC 11)	Research Methodology and IPR (20 MMAC 11)
20-09-2023 (2.00 PM to 5.00 PM (Wednesday)	Power Quality Improvement (20 MPE 015 D)	Design for Manufacture Assembly and Environments (20 MPDD14 A)	Advanced Digital System Design (20 MVSD14D)	Artificial Intelligence (20 MCSE 14 B)
22-09-2023 (2.00 PM to 5.00 PM (Friday)	Solar Energy Conversion System (20 MPE016B)	Design of Hybrid ad Electric Vehicles (20 MPDD15 C)	Design for Testability and Testing (20 MVSD15A)	Advanced Compile Design (20 MCSE 15 B)

Controller of examinations

Copy to

1) The Dean (Academics)

2) The HODs of EEE, MECH, ECE, CSE: with a request to arrange to read out in the respective class rooms and circulation among the faculty members and display on the notice board.

3) Copy to Computer Centre to upload the same in the college website.

4) Copy to Maintenance, Transport and Electrical Departments.