

## SREENIVASA INSTITUTE OF TECHNOLOGY AND MANAGEMENT STUDIES I M.TECH II SEMESTER I INTERNAL EXAMS- AUGUST -2022

R-20

## **CIRCULAR**

DATE: 29-07-2022

**Sub:** -First Internal Examination for **I M.Tech II SEM**- Schedule-Reg.

It is hereby informed that First Mid Examination for I M.Tech- II SEM is going to be held from **04-08-2022 To 06-08-2022.** The time table is enclosed herewith the circular. The allotment of rooms will be displayed in the notice board, on the day of the examinations.

## **General Instructions:**

- First Mid-Term examination will be conducted in the Units-I and Unit-II of syllabus.
- Question paper consists of descriptive paper for 30 marks with duration of 2 hours.
- Descriptive paper shall contain 3 questions with internal choice by choosing at least one question from each unit. Each question carries 10 marks.
- Question paper must be prepared as per Regulation's guidelines, Format, Blooms Taxonomy, Course Outcomes and Program out Comes..
- Question paper must be prepared as per Regulation's guidelines, Format, Blooms Taxonomy,
  Course Outcomes and Program out Comes.
- ❖ The faculty members are requested to submit soft copy of the question papers along with undertaking form to the (internalcell@sitams.org) internal examination cell by on or before <u>03-08-2022</u> without fail.
- The Softcopy of the question paper must be named with Course Code & Course Title (e-g
   20MCSE 21-Data Warehousing and Mining)
- For multiple sections, one common question paper must be prepared.

CONTROLLER OF EXAMINAITONS

Copy to

- 1) The Dean (Academics)
- 2) The HODs of EEE, MECH, ECE, CSE: with a request to arrange to read out in the respective class rooms and circulation among the faculty members and display on the notice board.
- 3) Copy to Computer Centre to upload the same in the college website.
- 4) Copy to Maintenance, Transport and Electrical Departments.

## SREENIVASA INSTITUTE OF TECHNOLOGY & MANAGEMENT STUDIES I M.Tech II SEM I - MID EXAM SCHEDULE -AUGUST- 2022 Date: 29-07-2022

R-20

		PE	VLSI	МЕСН
DATE / TIME	CSE	<u></u>		Integrated Product Design and Development
04-08-2022 (10.00 AM To 12.00 Noon)	Data Warehousing and Mining (20MCSE21)	Modern Rectifiers And Resonant Converters (20MPE021)	Low Power VLSI Design (20MVSD21)	(20MPDD21)
		COMPONENT		Additive Manufacturing Processes
04-08-2022 (2.00 PM To 4.00 PM)	Soft Computing (20MCSE22)	Power Electronic Control Of AC drives.(20MPE0 22)	ASIC Design (20MVSD22)	(20MPDD22)
	Big Data Analytics	Digital Control Of Power Electronics	Scripting Languages for HDL (20MVSD23)	Quality Concepts in Product Design (20MPDD23)
05-08-2022 (10.00 AM To 12.00 Noon)	(20MCSE23)	And Drives. (20MPE0 23)	(200110020)	Land Ergonomics
05-08-2022	Optimization Techniques (20MCSE24)	Modern Power Electronics (20MPE0 24)	CAD for VLSI (20MVSD24)	Industrial Design and Ergonomics (20MPDD24)
(2.00 PM To 4.00 PM)	(ZUMCSEZ+)	a inadustor	- de la desposición	Product Innovation and Management
06-08-2022 (10.00 AM To 12.00 Noon)	Cryptography and Network Security (20MCSE25C)	Advanced Power Semiconductor Devices and Protection (20MPE0 25A)	Microelectronics (20MVSD25C)	(20MPDD25A)
			Advanced Computer	Marketing Research for New Product
06-08-2022 (2.00 PM To 4.00 PM)	Smart Sensors and Internet of Things (20MCSE26D)	Advanced Drives and Control (20MPE026B)	Architecture (20MVSD26B)	(20MPDD26A)
				Principal

Controller of examinations

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