



SREENIVASA INSTITUTE OF TECHNOLOGY AND MANAGEMENT STUDIES
(AUTONOMOUS)

I M.TECH II SEMESTER II INTERNAL EXAMS- OCTOBER-2020

CIRCULAR

DATE: 24-09-2020

Sub: - First Internal Examination for I M.Tech II SEM – Schedule –Reg.

It is hereby informed that First Internal Examination for I M.Tech II Sem scheme is scheduled from **05-10-2020 To 10-10-2020**. The time table is enclosed. The allotment of rooms will be displayed in the Notice Board. The exam is from **2.00 PM To 4.00 PM**.

The Exam invigilation schedules are here by forwarded to the HOD's for reference and circulation. The faculty members are requested to note down the dates and the question papers should reach the internal cell on or before **01-10-2020** without fail.

First internal examination shall be conducted in units-I and Unit-II .The question paper consists of 5 short answer questions for 10 marks and 3 descriptive questions with internal choice for 30 marks with a total duration of 2 hours. Finally the marks obtained (short answer questions and descriptive) for 40 will be reduced to 15 marks.


CONTROLLER OF EXAMINATIONS



PRINCIPAL

Copy to

- i) Principal
- ii) All the HODs
- iii) Maintenance, Transport and Electrical Departments



DATE / TIME	CSE	VLSI	C S
05-10-2020 (2.00 PM To 4.00 PM)	Data Warehousing and Mining (13 MCSE 21)	Testing & Testability (13 MVSD 21)	Wireless Communication and Fading (13 MCOS 21)
06-10-2020 (2.00 PM To 4.00 PM)	Software Quality Assurance and Testing (13 MCSE 22)	Low Power VLSI Design (13 MVSD 22)	Digital Satellite Communication (13 MCOS 22)
07-10-2020 (2.00 PM To 4.00 PM)	Software Architecture and Design Patterns (13 MCSE 24)	Algorithms for VLSI Design Automation (13 MVSD 23)	Detection and Estimation of Signals (13 MCOS 23)
08-10-2020 (2.00 PM To 4.00 PM)	Ad-hoc and Sensor Network Security (13 MCSE 25)	FPGA Architecture & Applications (13 MVSD 24)	Optical Fiber Communications (13 MCOS 24)
09-10-2020 (2.00 PM To 4.00 PM)	Cryptography and Network Security (13 MCSE 25)	Scripting Language for VLSI Design Automation (13 MVSD 25)	RF Systems and Circuits (13 MCOS 25)
10-10-2020 (2.00 PM To 4.00 PM)	Elective-II ➤ Semantic Web (13 MCSE 26 C) ➤ Distributed Databases (13 MCSE 26 A) ➤ Service Oriented Architecture (13 MCSE 26 B)	Elective-II ➤ Nano Electronicss (13 MVSD 26 A) ➤ Cryptography & Network Security (13 MVSD 26 B) ➤ Hardware Software Codesign (13 MVSD 26 C)	Elective-II ➤ MIMO Communication Systems (13 MCOS 26 A) ➤ Secure Communications (13 MCOS 26 B) ➤ Image and Video Processing (13 MCOS 26 C)


Controller of examinations


Principal