

SREENIVASA INSTITUTE OF TECHNOLOGY AND MANAGEMENT STUDIES I M.TECH II SEMESTER II INTERNAL EXAMS- SEPTEMBER -2022

R-20

CIRCULAR

DATE: 20-09-2022

Sub: - Second Internal Examination for I M.Tech II SEM - Schedule - Reg.

It is hereby informed that Second Mid Examination for I M.Tech- II SEM is going to be held from 29-09-2022 To 01-10-2022. The time table is enclosed herewith the circular. The allotment of rooms will be displayed in the notice board, on the day of the examinations.

General Instructions:

- Second Mid-Term examination will be conducted in the Units-III, Unit-IV and Unit-V of syllabus.
- Question paper consists of descriptive paper for 30 marks with duration of 2 hours.
- Descriptive paper shall contain 3 questions with internal choice by choosing at least one question from each unit. Each question carries 10 marks.
- Question paper must be prepared as per Regulation's guidelines, Format, Blooms Taxonomy, Course Outcomes and Program out Comes.
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- ❖ The faculty members are requested to submit soft copy of the question papers along with undertaking form to the (internalcell@sitams.org) internal examination cell by on or before <u>28-09-2022</u> without fail.
- The Softcopy of the question paper must be named with Course Code & Course Title (e-g 20MCSE 21-Data Warehousing and Mining)

For multiple sections, one common question paper must be prepared.

CONTROCLER OF EXAMINAITONS

Copy to

- 1) The Dean (Academics)
- 2) The HODs of EEE, MECH, ECE, CSE: with a request to arrange to read out in the respective class rooms and circulation among the faculty members and display on the notice board.
- Copy to Computer Centre to upload the same in the college website.
- 4) Conv to Maintenance Transport and Flectrical Departments



SREENIVASA INSTITUTE OF TECHNOLOGY & MANAGEMENT STUDIES I M.Tech II SEM II-MID EXAM SCHEDULE -SEPTEMEBER- 2022 Date: 20-09-2022

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DATE / TIME	CSE	PE	VLSI	MECH-PD
29-09-2022 (10.00 AM To 12.00 Noon)	Data Warehousing and Mining (20MCSE21)	Modern Rectifiers And Resonant Converters (20MPE021)	Low Power VLSI Design (20MVSD21)	Integrated Product Design and Development (20MPDD21)
29-09-2022	Soft Computing	Power Electronic Control Of AC	ASIC Design	Additive Manufacturing Processes (20MPDD22)
(2.00 PM To 4.00 PM)	(20MCSE22)	drives.(20MPE0 22)	(20MVSD22)	
30-09-2022	Big Data Analytics	Digital Control Of Power Electronics	Scripting Languages for HDL	Quality Concepts in Product Design
(10.00 AM To 12.00 Noon)	(20MCSE23) ,	And Drives. (20MPE0 23)	(20MVSD23)	(20MPDD23)
30-09-2022	Optimization Techniques	Modern Power Electronics	CAD for VLSI	Industrial Design and Ergonomics
(2.00 PM To 4.00 PM)	(20MCSE24)	(20MPE0 24)	(20MVSD24)	(20MPDD24)
01-10-2022 (10.00 AM To 12.00 Noon)	Cryptography and Network Security (20MCSE25C)	Advanced Power Semiconductor Devices and Protection (20MPE0 25A)	Microelectronics (20MVSD25C)	Product Innovation and Management (20MPDD25A)
01-10-2022	Smart Sensors and Internet of	Advanced Drives and Control	Advanced Computer,	Marketing Research for New Product
(2.00 PM To 4.00 PM)	Things (20MCSE26D)	(20MPE026B)	Architecture (20MVSD26B)	(20MPDD26A)

Controller of examinations

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Principal