

**SREENIVASA INSTITUTE OF TECHNOLOGY AND MANAGEMENT STUDIES, CHITTOOR  
(AUTONOMOUS)  
Murukambattu, CHITTOOR – 517 127**

(Approved by AICTE, New Delhi & Affiliated to JNT University, Anantapuramu)



**EXAMINATION SECTION**

Date: 23/02/2023

**Notification  
for  
I year M.Tech I Semester (R20)**

**Regular/Supplementary Examinations March-2023**

The students appearing for the above Practical examinations commencing **04-03-2023** to **08-03-2023** and Theory examinations commencing **09-03-2023** are informed that the applications will be received as per the time schedule given below:

<b>Without late fee</b>	<b>01<sup>st</sup> March -2023</b>
<b>With late fee of Rs. 500/-</b>	<b>04<sup>th</sup> March -2023</b>

Examination Fee		
<b>For the whole Examinations</b>	<b>For Regular only</b>	<b>Rs.1500-00</b>
<b>For supplementary Examinations</b>	<b>each theory / Laboratory</b>	<b>Rs.700-00</b>

**Note:**

- (i) Applications are available in the Examination Section
- (ii) The Fee should be paid through **AXIS BANK, Chittoor Main Branch, SITAMS** examination **Account No:922010002222576** , **IFSC Code: UTIB0000275** through challan.
- (iii) Challans along with application have to be submitted in the examination section
- (iv) Hall tickets will be issued to only the eligible candidates who fulfill the academic requirements.

  
**Controller of Examinations**

  
**Principal**

1. The Dean (Academics)
2. Copy to EEE, ME, ECE & CSE HODs with a request to arrange to read in the respective class room and circulation among the faculty members and display on the notice board.
3. Copy to Computer Centre to upload the same in the college website



**SREENIVASA INSTITUTE OF TECHNOLOGY & MANAGEMENT STUDIES (AUTONOMOUS) CHITTOOR**  
**I YEAR M.TECH I SEMESTER REGULALR/SUPPLEMENTARY (R20) -MARCH-2023 EXAMINATIONS**

**R20**

**Time-table**

**Dt: 24-02-2023**

DATE / TIME	Power Electronics (EEE)	Product Design, (ME)	VLSI System Design (ECE)	CSE
09-03-2023 (10.00 AM to 01.00 PM) (Thursday)	Analysis of Power Electronic Converter (20 MPE011)	Creativity in Product Design (20 MPDD11)	CMOS VLSI and Digital IC Design (20MVSD11)	Mathematical Foundations of Computer Science (20MCSE11)
11-03-2023 (10.00 AM to 01.00 PM) (Saturday)	Power Electronic Control of DC Drives (20 MPE012)	Computer Applications in Product Design (20 MPDD 12)	Analog IC Design (20 MVSD12)	Advanced Data Structures and Algorithms (20 MCSE 12)
13-03-2023 (10.00 AM to 01.00 PM) (Monday)	Control System Design for Power Electronics (20 MEPE 13)	Material Selection for Product Development (20 MPDD 13)	FPGA and HDL Programming (20 MVSD13)	Java and Web Technologies (20 MCSE 13)
15-03-2023 (10.00 AM to 01.00 PM) (Wednesday)	Research Methodology and IPR (20 MMAC 11)	Research Methodology and IPR (20 MMAC 11)	Research Methodology and IPR (20 MMAC 11)	Research Methodology and IPR (20 MMAC 11)
17-03-2023 (10.00 AM to 01.00 PM) (Friday)	Power Quality Improvement (20 MPE 015 D)	Design for Manufacture Assembly and Environments (20 MPDD14 A)	Advanced Digital System Design (20 MVSD14D)	Artificial Intelligence (20 MCSE 14 B)
20-03-2023 (10.00 AM to 01.00 PM) (Monday)	Solar Energy Conversion System (20 MPE016B)	Design of Hybrid ad Electric Vehicles (20 MPDD15 C)	Design for Testability and Testing (20 MVSD15A)	Advanced Compiler Design (20 MCSE 15 B)

**Controller of examinations**

Copy to

- 1) The Dean (Academics)
- 2) The HODs of EEE, MECH, ECE, CSE: with a request to arrange to read out in the respective class rooms and circulation among the faculty members and display on the notice board.
- 3) Copy to Computer Centre to upload the same in the college website.
- 4) Copy to Maintenance, Transport and Electrical Departments.

**Principal**