



SREENIVASA INSTITUTE OF TECHNOLOGY & MANAGEMENT STUDIES
I M.Tech I SEMESTER II- MID EXAMINATION SCHEDULE-MAYH-2022
TIMETABLE

R-20

Date: 11-05-2022

DATE / TIME	CSE	PE (EEE)	VLSI (ECE)	MECH
18-05-2022 (10.00 AM To 12.00 Noon)	Mathematical Foundations of Computer Science (20 MCSE 11)	Analysis of Power Electronic Converter (20 MPE011)	CMOS VLSI and Digital IC Design (20 MVSD 11)	Creativity in Product Design (20 MPDD11)
18-05-2022 (2.00 PM To 4.00 PM)	Advanced Data Structures and Algorithms (20 MCSE 12)	Power Electronic Control of DC Drives (20 MPE012)	Analog IC Design (20 MVSD 12)	Computer Applications in Product Design (20 MPDD 12)
19-05-2022 (10.00 AM To 12.00 Noon)	Java and Web Technologies (20 MCSE 13)	Control System Design for Power Electronics (20 MEPE 13)	FPGA and HDL Programming (20 MVSD 13)	Material Selection for Product Development (20 MPDD 13)
19-05-2022 (2.00 PM To 4.00 PM)	Research Methodology and IPR (20 MMAC 11)	Research Methodology and IPR (20 MMAC 11)	Research Methodology and IPR (20 MVSD 16)	Research Methodology and IPR (20 MMAC 11)
20-05-2022 (10.00 AM To 12.00 Noon)	Artificial Intelligence (20 MCSE 14 B)	Power Quality Improvement (20 MPE 015 D)	Design for Testability and Testing (20 MVSD 15)	Design for Manufacture Assembly and Environments (20 MPDD14 A)
20-05-2022 (2.00 PM To 4.00 PM)	Advanced Compiler Design (20 MCSE 15 B)	Solar Energy Conversion System (20 MPE016B)	Advanced Digital System Design (20 MVSD 14)	Design of Hybrid and Electric Vehicles (20 MPDD15 C)

Controller of examinations

Copy to

- 1) The Dean (Academics)
- 2) The HODs of EEE, MECH, ECE & CSE: with a request to arrange to read out in the respective class rooms and circulation among the faculty members and display on the notice board.
- 3) Copy to Computer Centre to upload the same in the college website.
- 4) Copy to Maintenance, Transport and Electrical Departments.

Principal